

FIG. 1

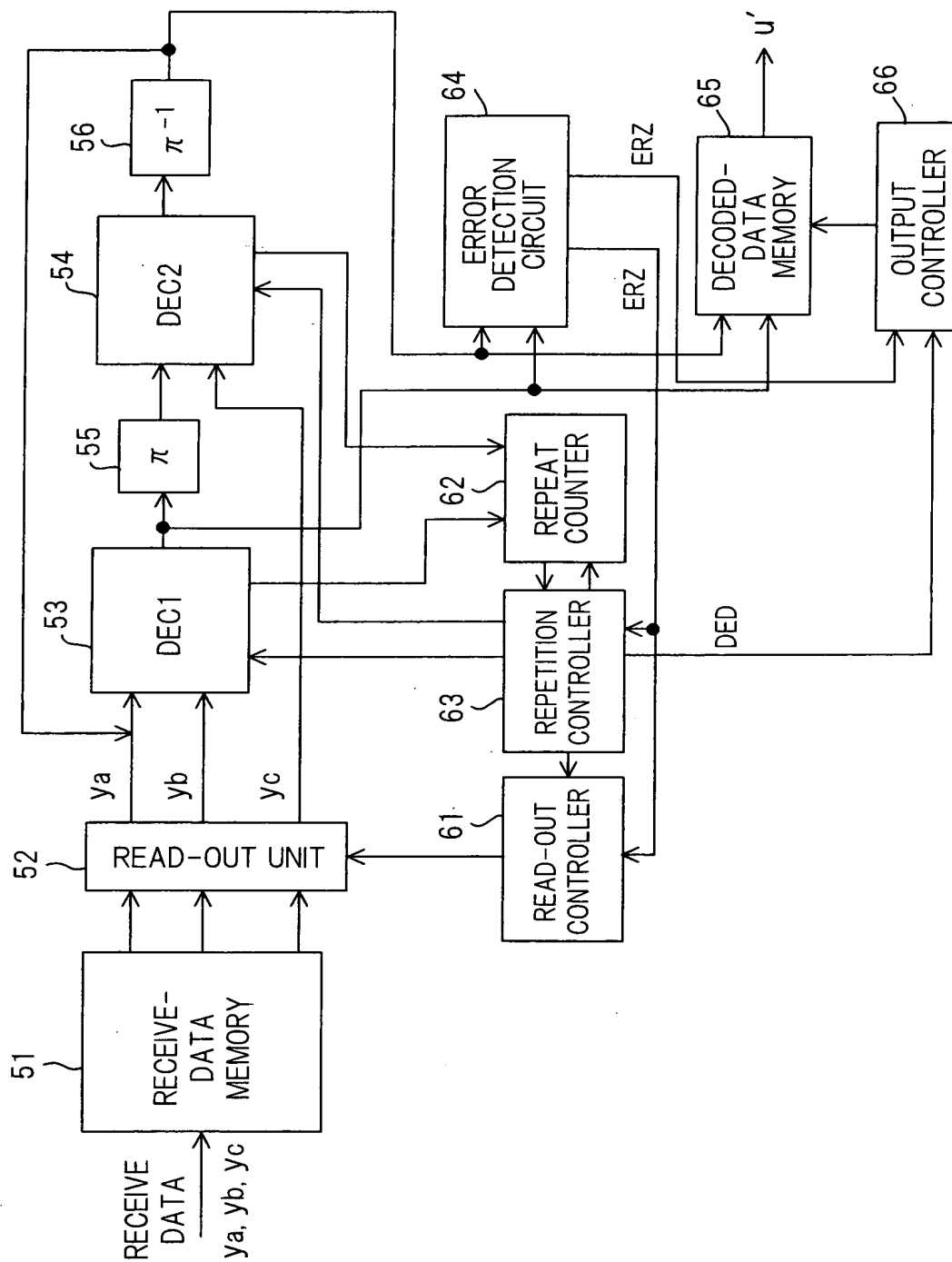


FIG. 2

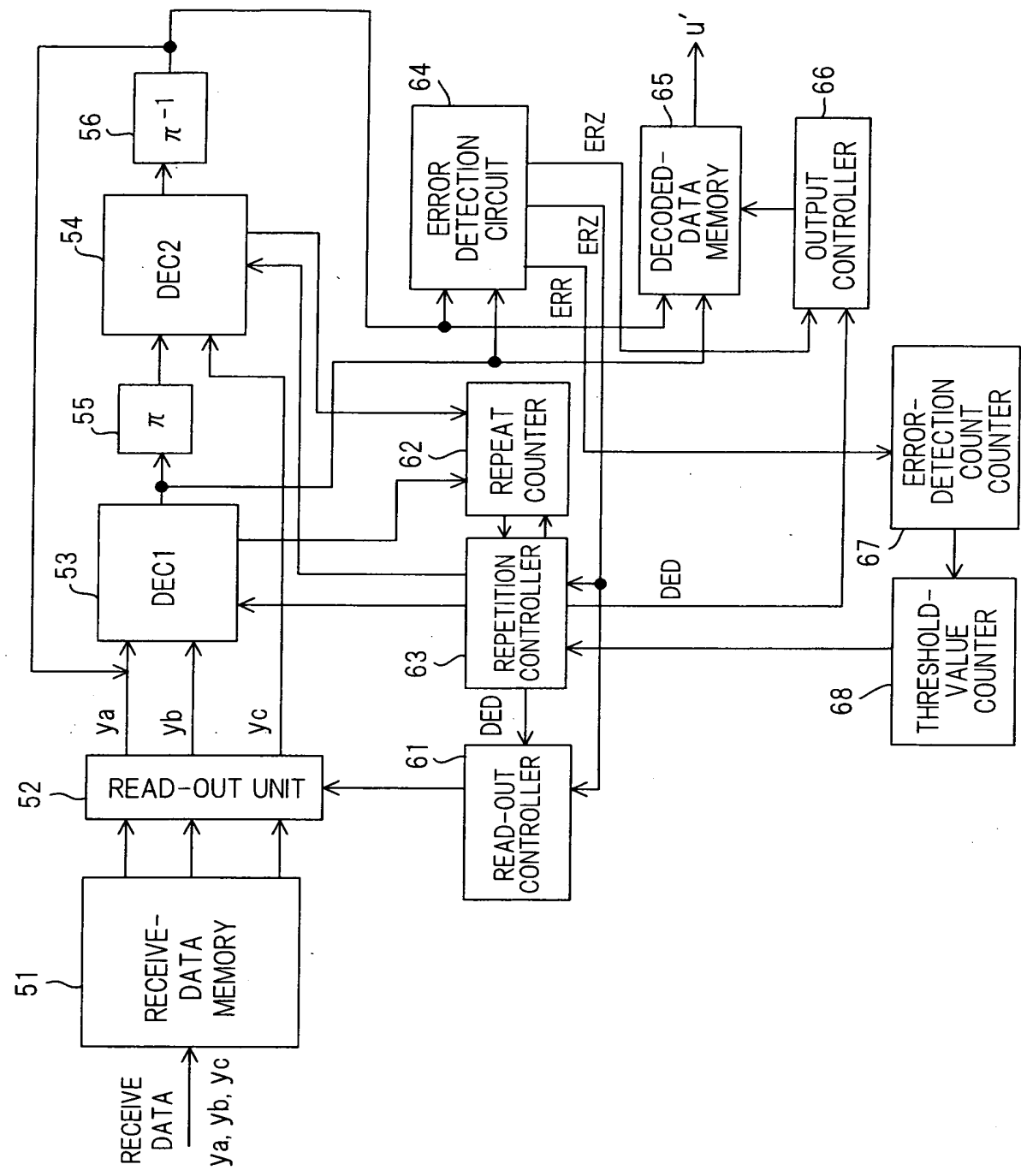


FIG. 3

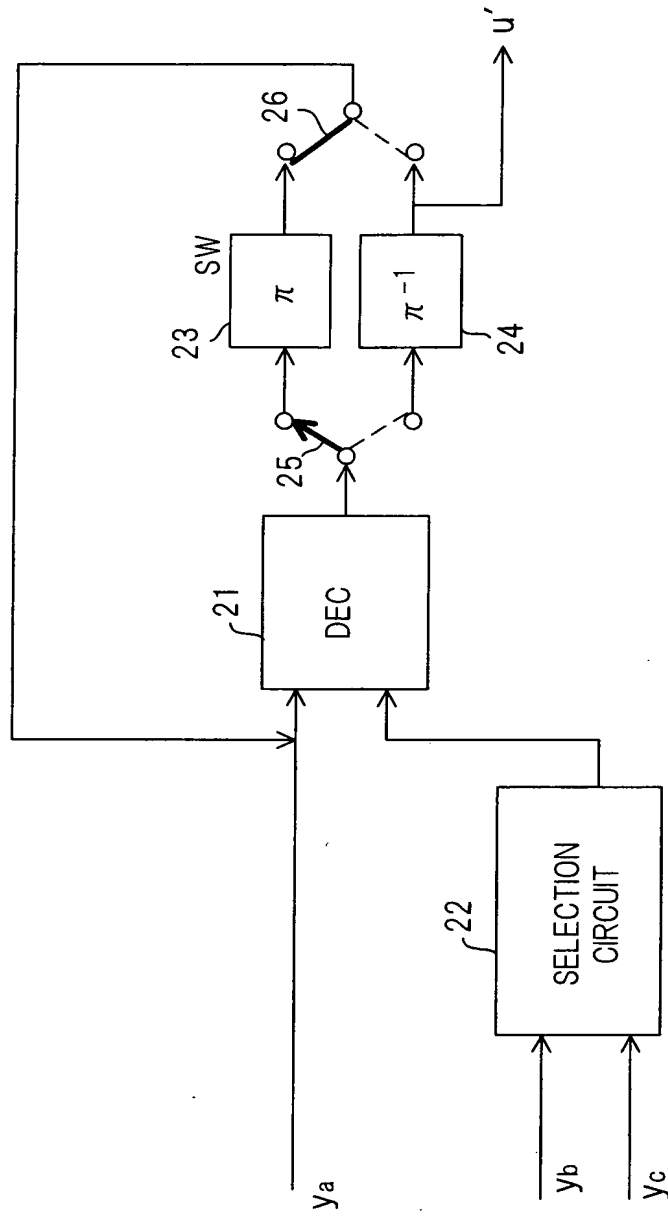


FIG. 4

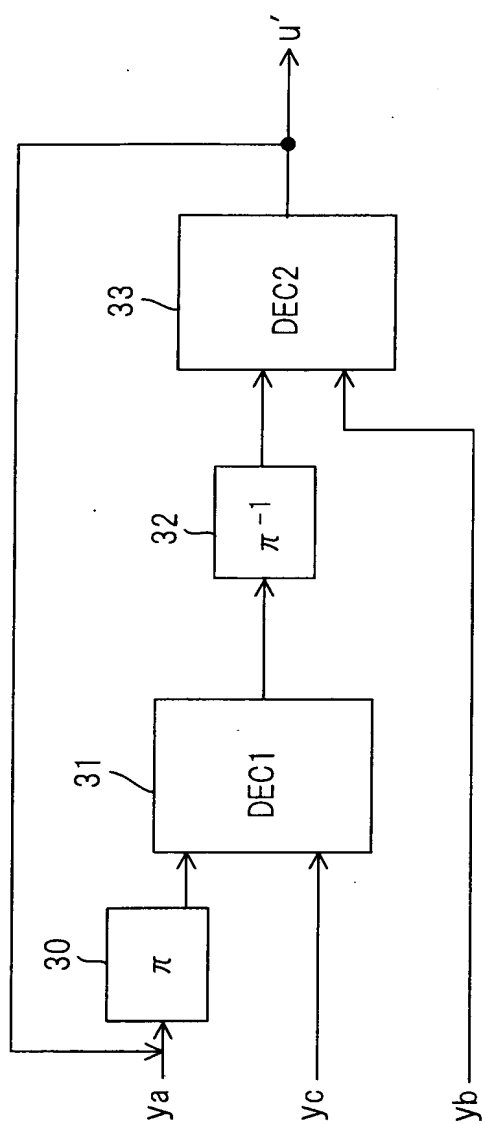
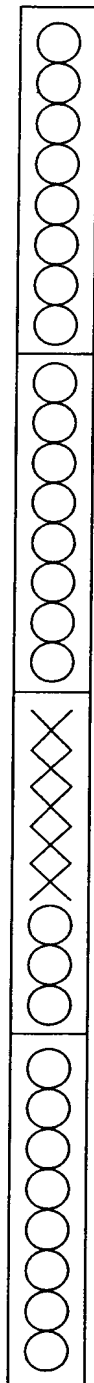


FIG. 5

ERROR PATTERN ACCORDING TO THIS EMBODIMENT

ERROR ON PER-BIT BASIS



ERROR ON PER-INFORMATION-BLOCK BASIS



FIG. 6

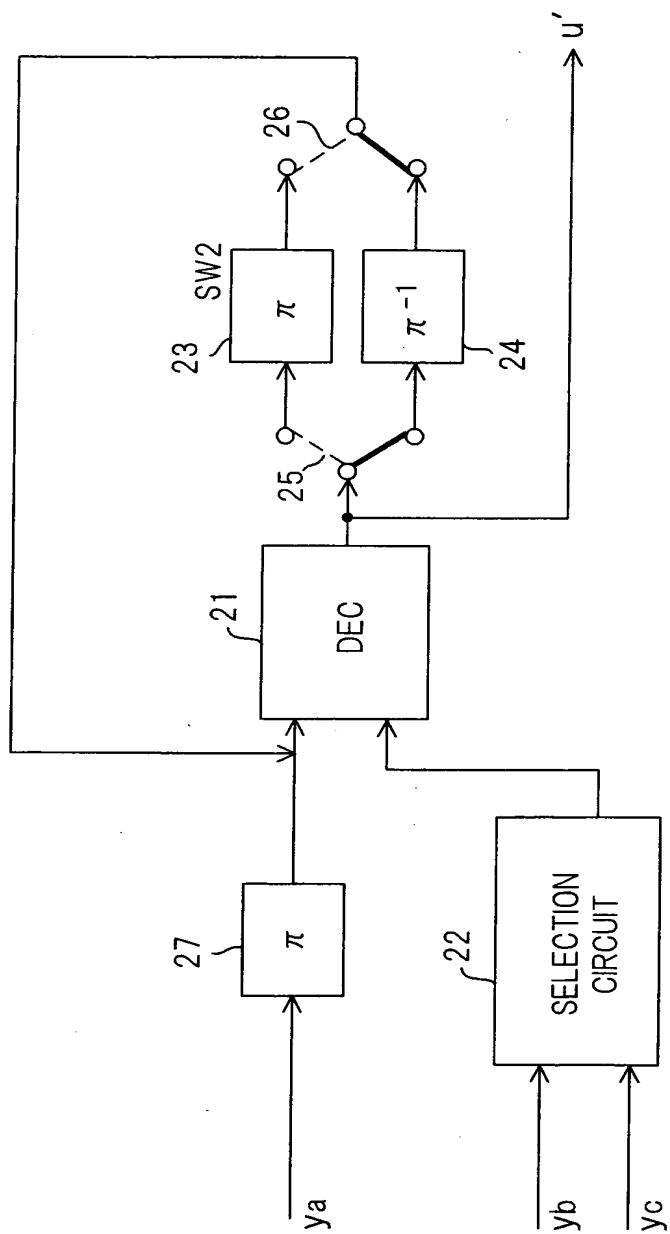


FIG. 7

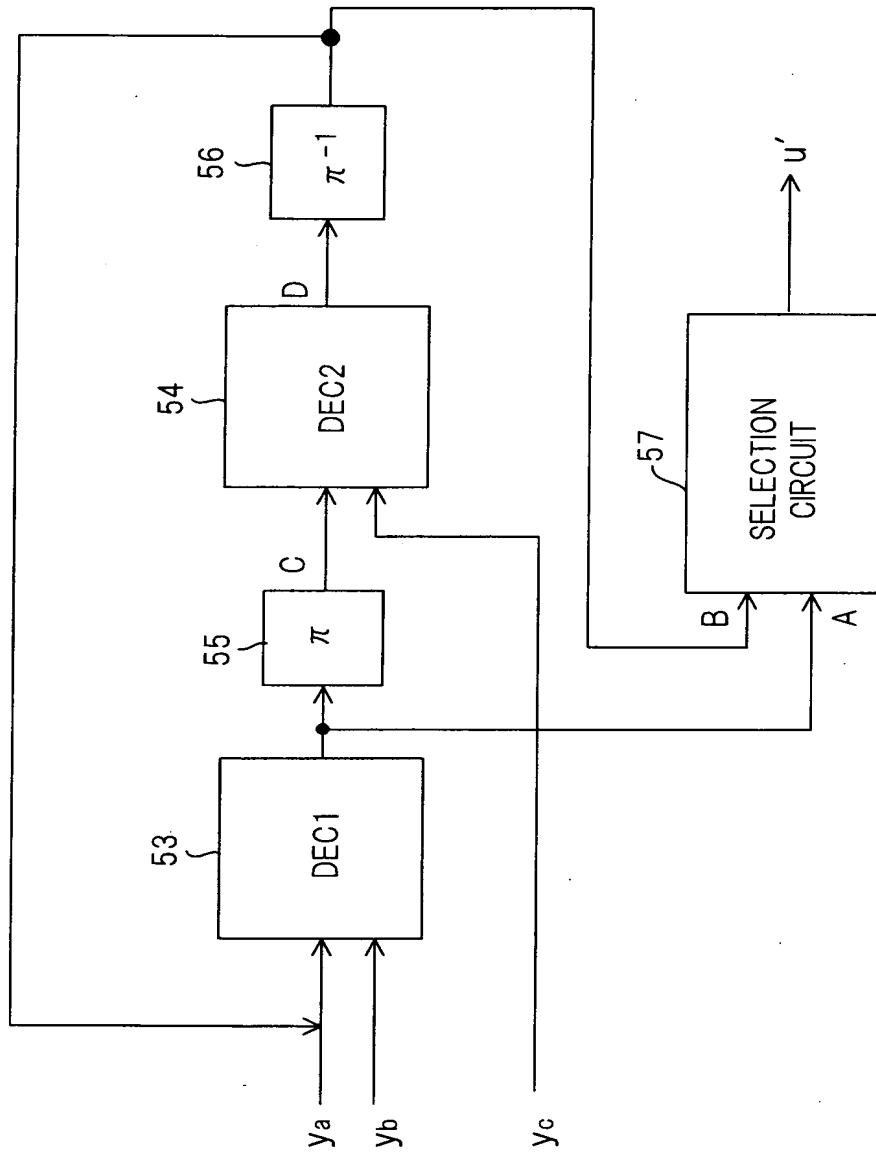
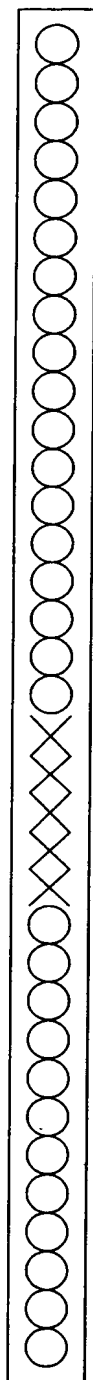
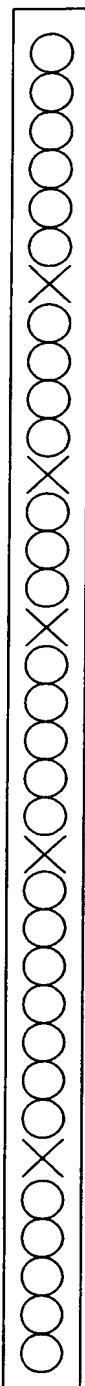


FIG. 8

ERROR PATTERNS AT OUTPUTS (A, D) OF DECODERS



ERROR PATTERNS AT OUTPUTS (B, C) OF INTERLEAVERS (π , π^{-1})



○ : CORRECT X : ERROR

FIG. 9

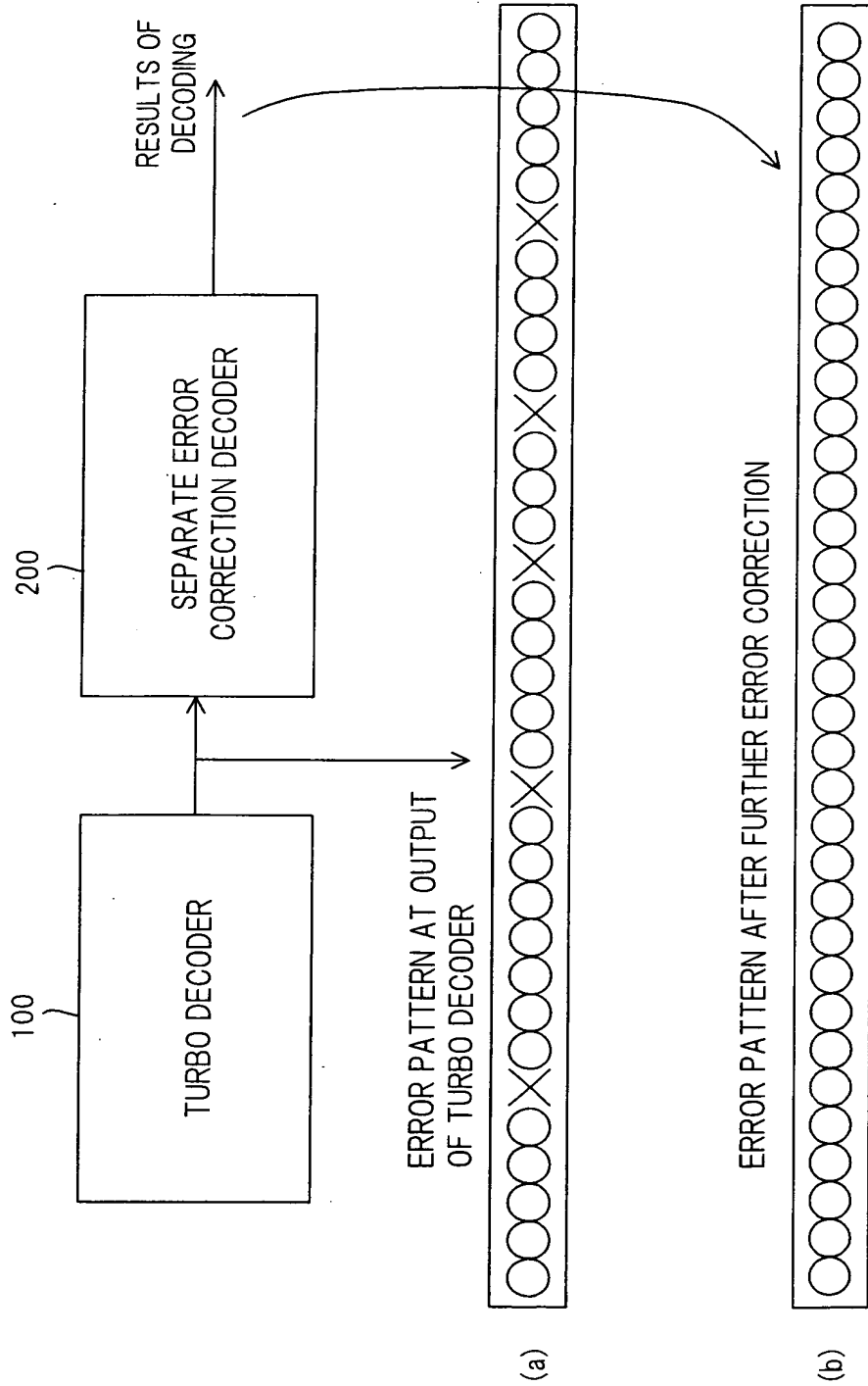


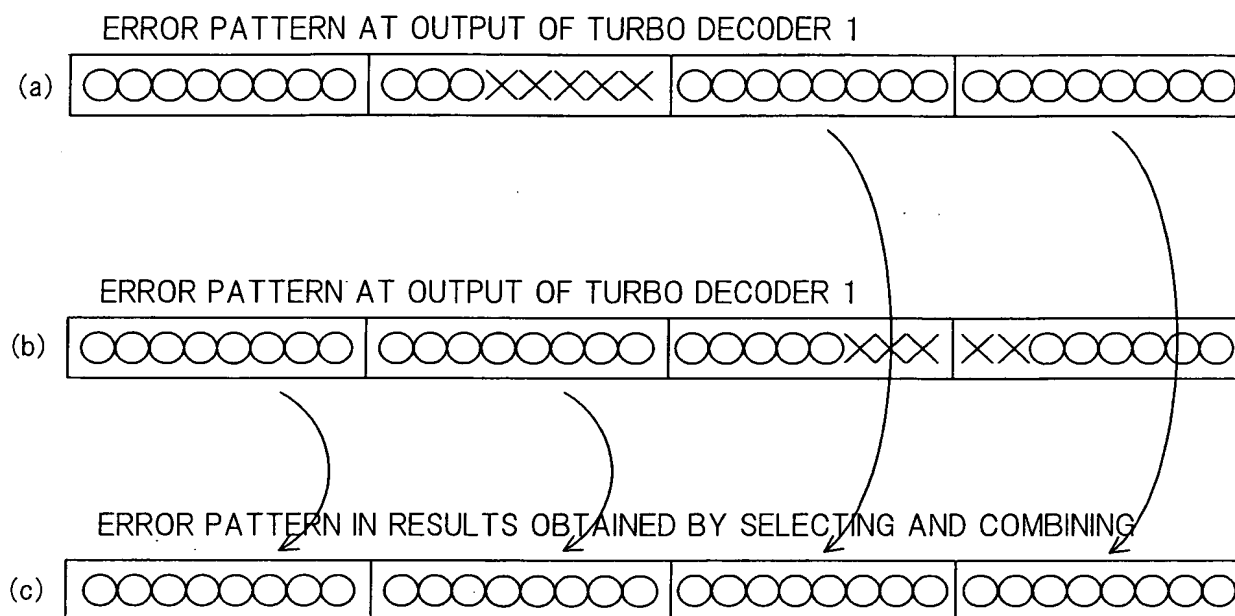
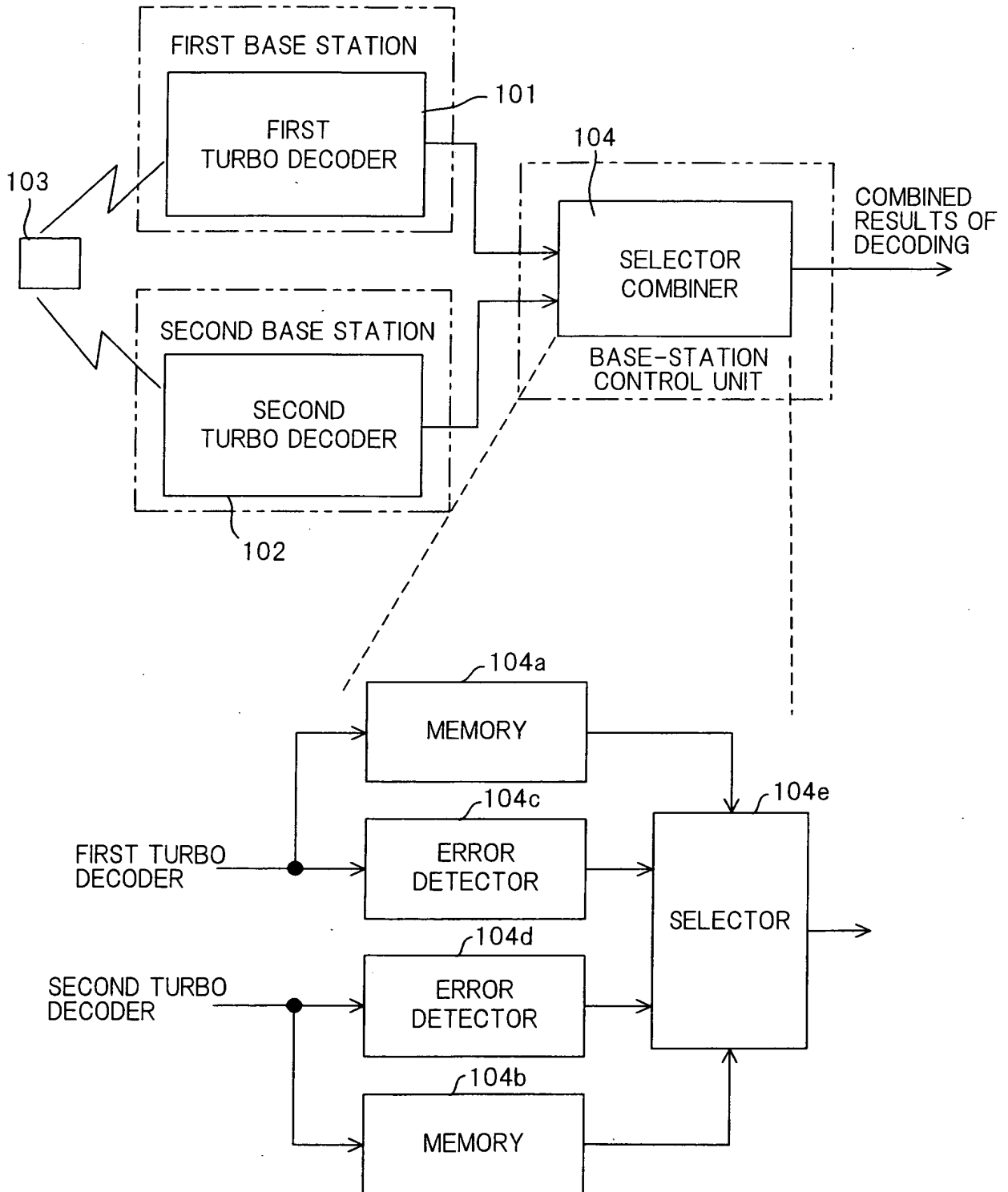
FIG. 10

FIG. 11

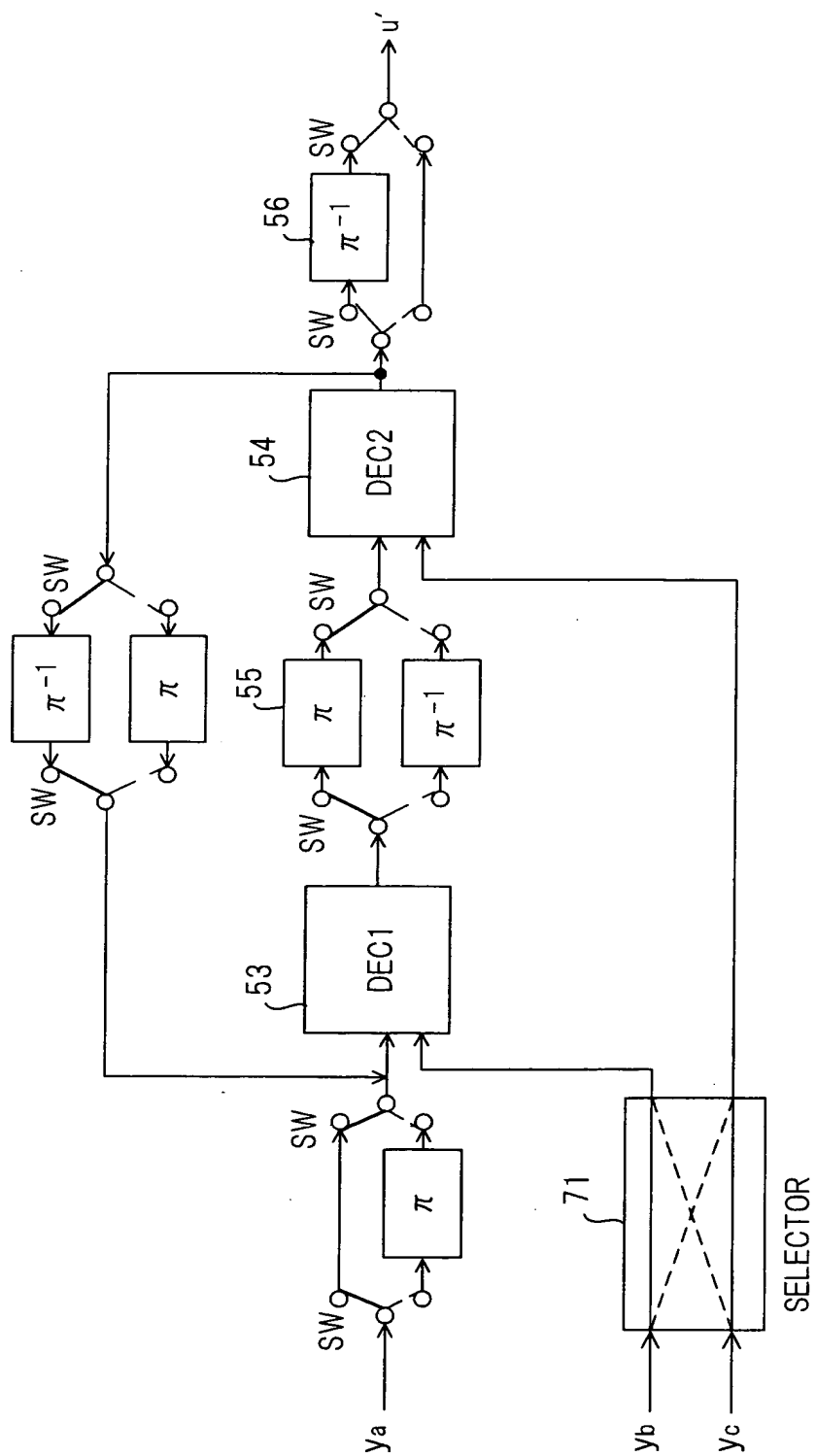


FIG. 13

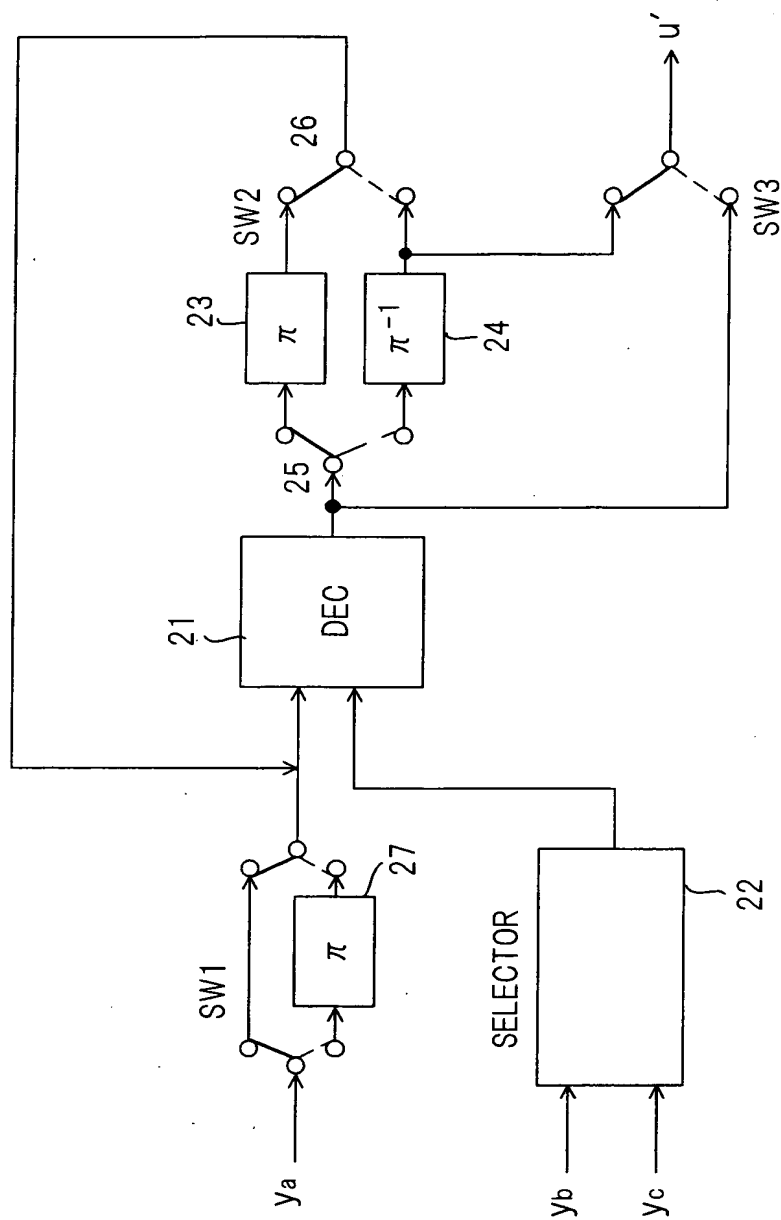
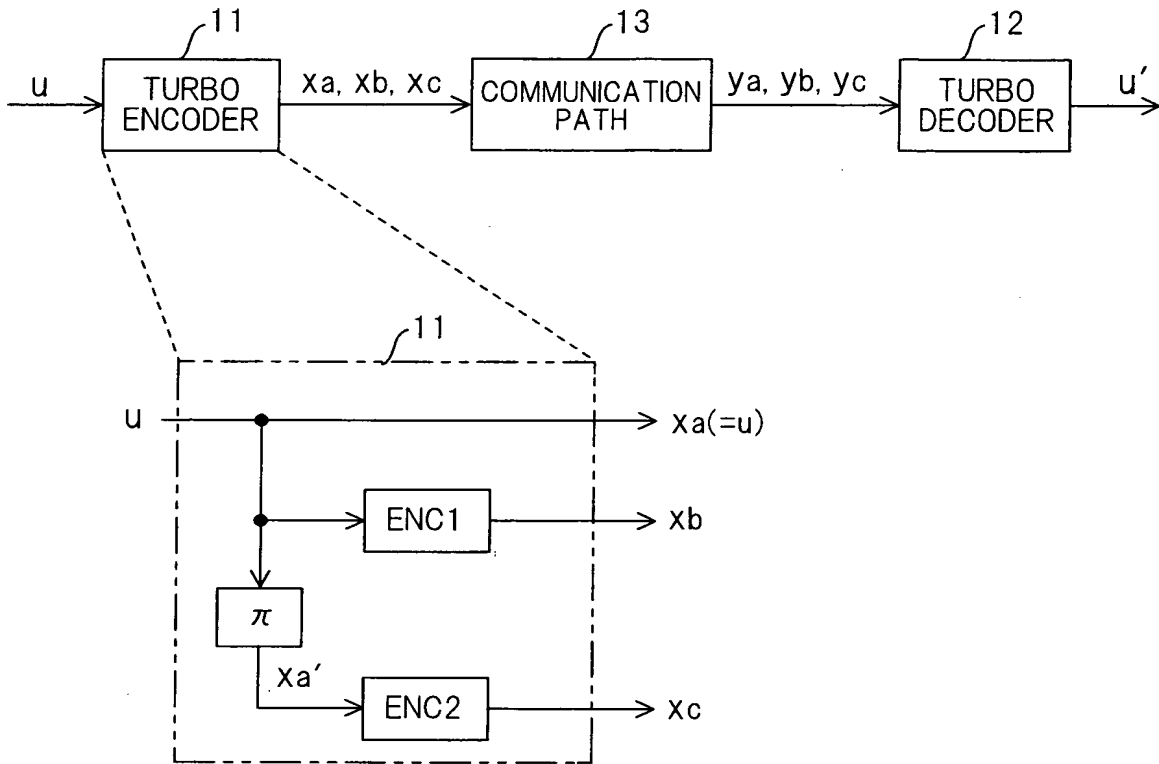


FIG. 14

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FIG. 15

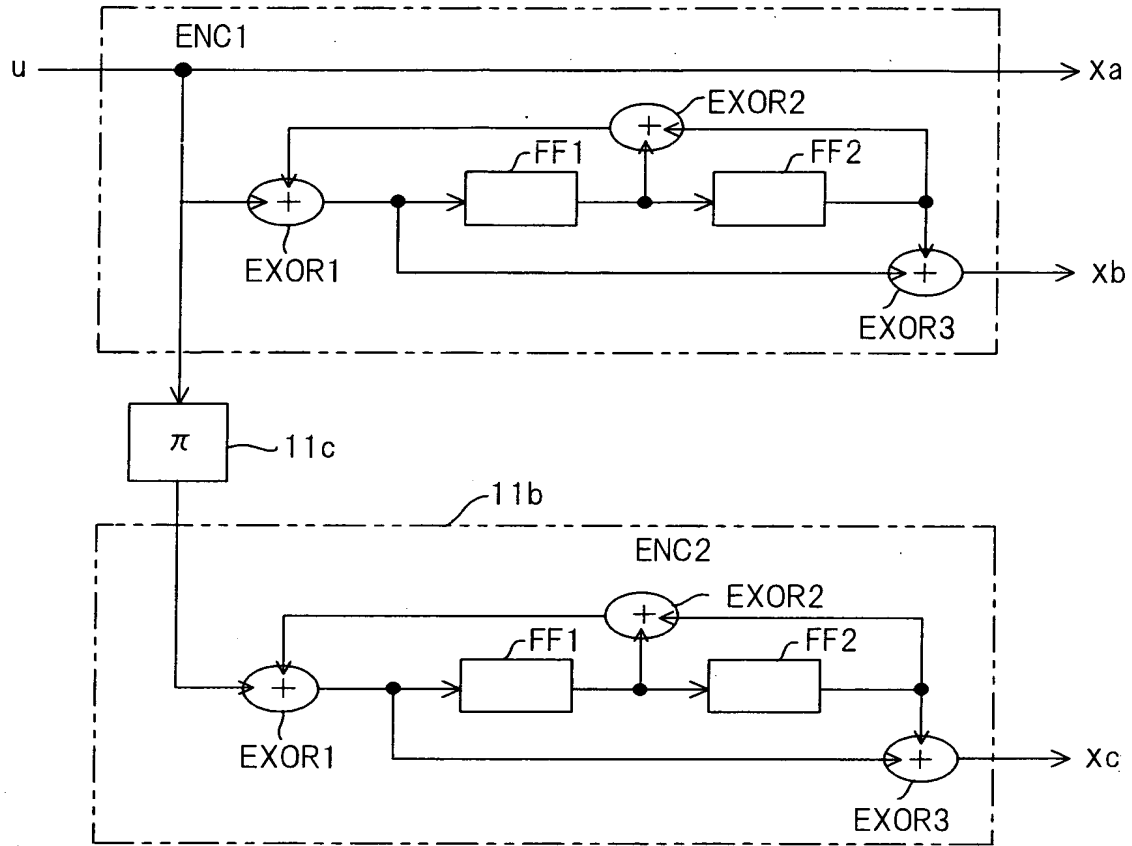


FIG. 16

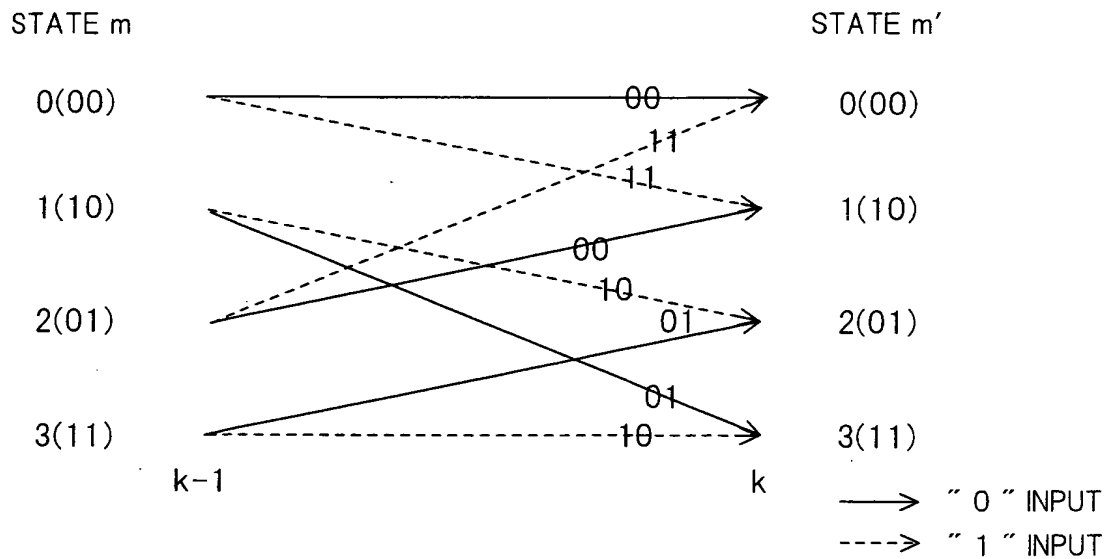


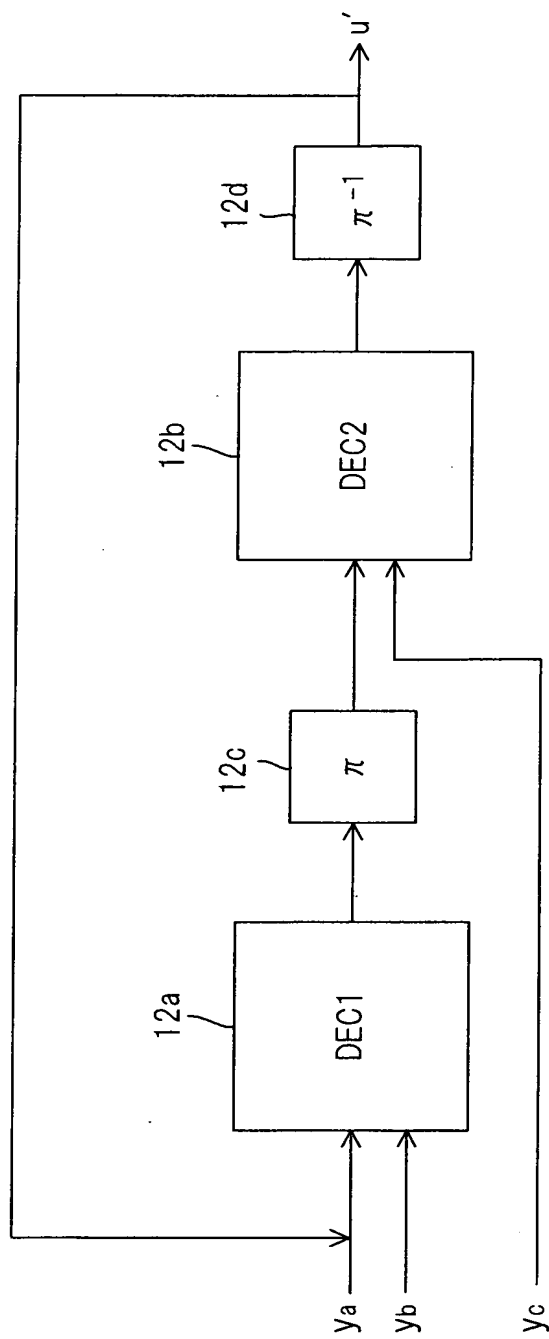
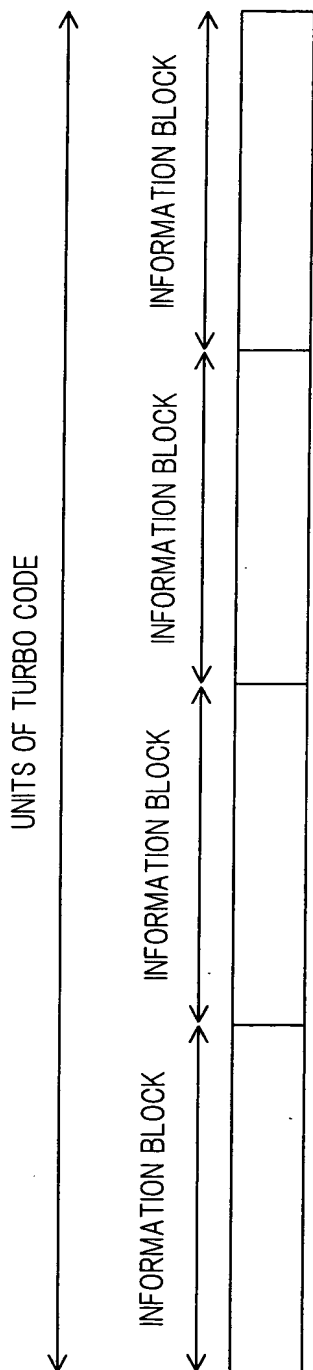
FIG. 17

FIG. 18

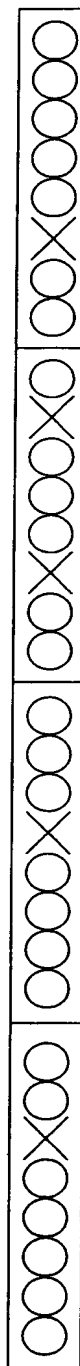
(a)



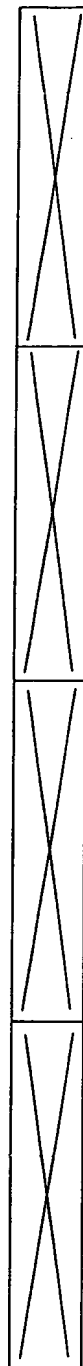
(b)

ERROR PATTERN ACCORDING TO PRIOR ART

ERROR ON PER-BIT BASIS



ERROR ON PER-INFORMATION-BLOCK BASIS



○ : CORRECT

× : ERROR